



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): **Hyeon-Seag Kim**

Application Serial No.: **09/877,906**

Filed: **June 9, 2001**

Title: **MOSFET with SiGe Source/Drain  
Regions and Epitaxial Gate  
Dielectric**

**TRANSMITTAL OF FORMAL DRAWINGS**

Commissioner for Patents  
Official Draftsperson  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir/Madam:

Enclosed, please find four (4) sheets of formal drawings for the above-identified application, which include amendments to Figures 1-10 as required by the Notice of Draftsperson's Patent Drawing Review (PTO-948), dated April 2, 2004. Issue fee payment has been submitted under separate cover. An early issuance of the above-identified application is respectfully requested.

Respectfully Submitted;  
FARJAMI & FARJAMI LLP

Dated: 4/28/04



Michael Farjami, Esq.  
Reg. No. 38,135  
FARJAMI & FARJAMI LLP  
Attorneys for Applicant

Michael Farjami, Esq.  
FARJAMI & FARJAMI LLP  
26522 La Alameda Ave., Suite 360  
Mission Viejo, California 92691  
Tel: (949) 282-1000  
Fax: (949) 282-1002

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